

ABSTRACT

The present invention provides a matched filter circuit, which has a small circuit scale, and can save power. An after-stage section of the matched filter circuit has n hold circuit groups $H21, H22, \dots H2n$ connected with an output signal $Dout1(i)$ of a pre-stage section in parallel, and the output of each hold circuit groups $H21$ to $H2n$ is connected individually to each multiplier $M21, M22, \dots M2n$. Each multiplier multiplies the output of each hold circuit group $H21$ to $H2m$ by each multiplier $d21, d22, \dots d2m$. The output of each multiplier circuit $M21$ to $M2n$ is inputted to an adder circuit $ADD2$, and then, the total sum $Dout2$ (correlation output) is calculated.

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